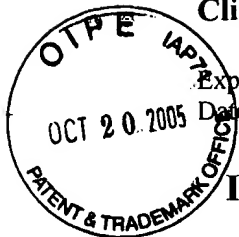


10-24-05

AP/2113  
PATENT

Attorney Docket No.: MTEC-003/00US  
Client Reference No.: 0120.00US



Express Mail Label Number: EV 525577209 US  
Date of Deposit: October 20, 2005

EV 525577209US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of THEKKATH

Serial No.: 09/844,669

Examiner: McCarthy, Christopher S.

Confirmation No.: 8984

Art Unit: 2113

Filed: April 30, 2001

For: **TRACE CONTROL FROM HARDWARE AND SOFTWARE**

**Mail Stop AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
TRANSMITTAL**

Enclosed is an Information Disclosure Statement and accompanying Form PTO/SB/08A for the above-identified patent application.

- ☒ In accordance with 37 C.F.R. §1.97(c), also enclosed is:
- ☒ the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p); or
  - ☐ a statement as specified in 37 C.F.R. §1.97(e).
- ☒ Check in the amount of \$180.00 for the total fee is attached.
- ☒ A return receipt postcard is also enclosed.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17, and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 03-3117.

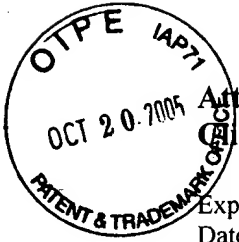
Dated: **October 20, 2005**

Cooley Godward LLP  
ATTN: Patent Group  
Five Palo Alto Square  
3000 El Camino Real  
Palo Alto, CA 94306-2155  
Tel: (650) 843-5000  
Fax: (650) 857-0663  
WSG:dm

Respectfully submitted,  
**COOLEY GODWARD LLP**

By:

William S. Galliani  
Reg. No. 33,885



Attorney Docket No.: MTEC-003/00US

PATENT

Client Reference No.: 0120.00US

Express Mail Label Number:

EV 525577209 US

Date of Deposit:

October 20, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of THEKKATH

Serial No.: 09/844,669

Examiner: McCarthy, Christopher S.

Confirmation No.: 8984

Art Unit: 2113

Filed: April 30, 2001

For: **TRACE CONTROL FROM HARDWARE AND SOFTWARE****Mail Stop AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. §1.97(c)**

In accordance with the duty of disclosure set forth in 37 C.F.R. §1.56,  
Applicant(s) hereby submits the following information in conformance with 37 C.F.R.  
§§1.97 and 1.98.

- ☒ Pursuant to 37 C.F.R. §1.98, a copy of each document **P5-P6**, and **D1-D16** cited in the attached Form PTO/SB/08 are enclosed.
- ☐ No copies of the publications listed on the attached Form PTO/SB/08A are being provided pursuant to 37 C.F.R. §1.98(d) because the publications were previously cited by or submitted to the Office in prior Application Serial No. \_\_\_ to which the above-identified application claims priority under 35 U.S.C. §120.
- ☒ No copies of any U.S. patents or U.S. patent application publications listed on the attached Form PTO/SB/08A are being provided pursuant to 37 C.F.R. §1.98.
- ☐ Publication(s) \_\_\_ listed on the attached Form PTO/SB/08A were cited in a foreign search or examination report corresponding to \_\_\_ application serial no. \_\_\_ and mailed on \_\_\_.

10/25/2005 HLE333 00000130 09844669

01 FC:1806

180.00 OP

- ☐ Enclosed is a copy of a non-English publication(s) \_\_\_\_\_. Pursuant to §609 of the M.P.E.P., Applicant submits the attached foreign search or examination report, which cites such non-English language publication(s).
- ☐ Enclosed is a copy of a non-English publication(s) \_\_\_\_\_. English language publication \_\_\_\_ (copy enclosed) claims priority from this non-English publication.
- ☐ Enclosed is an explanation of non-English publication(s) \_\_\_\_\_ for which an English translation is not available.
- ☐ Enclosed is an English translation of non-English publication(s) \_\_\_\_\_ cited in the attached Form PTO/SB/08A.
- ☐ Enclosed is a copy of pending patent Application Serial No. \_\_\_\_\_.

This Information Disclosure Statement is filed after the period specified in 37 C.F.R. § 1.97(b), but before the mailing of:

- ☒ a final action under 37 C.F.R. §1.113;
- ☐ a notice of allowance under 37 C.F.R. §1.311; or
- ☐ an action that otherwise closes prosecution in this application.

In accordance with 37 C.F.R. §1.97(c) also enclosed is:

- ☒ Fee under 37 C.F.R. §1.17(p) in the amount of \$180.00.

It is respectfully requested that the Examiner consider the above-noted information and return an initialed copy of the attached Form PTO/SB/08A to the undersigned.

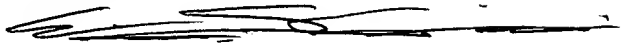
Dated: **October 20, 2005**

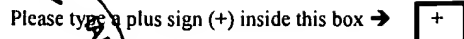
Cooley Godward LLP  
ATTN: Patent Group  
Five Palo Alto Square  
3000 El Camino Real  
Palo Alto, CA 94306-2155  
Tel: (650) 843-5000  
Fax: (650) 857-0663

WSG:dm

Respectfully submitted,  
**COOLEY GODWARD LLP**

By:

  
\_\_\_\_\_  
William S. Galliani  
Reg. No. 33,885



Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet

**1 of 1**

**Complete if Known**

Application Number	09/844,669
Filing Date	April 30, 2001
First Named Inventor	THEKKATH
Group Art Unit	2113
Examiner Name	McCarthy, Christopher S.
Attorney Docket Number	MTEC-003/00US

## U.S. PATENT DOCUMENTS

[illegible]

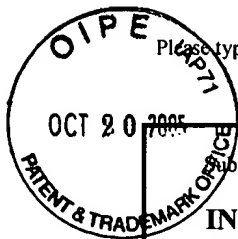
**Examiner  
Signature**

**Date  
Considered**

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number.

<sup>2</sup> See attached Kinds of U.S. Patent Documents.



Please type a plus sign (+) inside this box →



PTO/SB/08A (08-00)

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>	
		Application Number	09/844,669
		Filing Date	April 30, 2001
		First Named Inventor	THEKKATH
		Group Art Unit	2113
Sheet	1 of 1	Examiner Name	McCarthy, Christopher S.
		Attorney Docket Number	MTEC-003/00US

OTHER – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	D1	F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137.	
	D2	Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, <i>1990 ACM</i> , pp. 37-47.	
	D3	M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. 1-29.	
	D4	ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32.	
	D5	A. Srivastaba et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23.	
	D6	ATOM User Manual, Digital Equipment Corporation, March 1994, pp. 1-28.	
	D7	B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137.	
	D8	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, <i>ACM Computing Surveys</i> , Vol. 29, No. 2, June 1997, pp. 128-170.	
	D9	Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 <i>IEEE Service Center</i> , 12 pp.	
	D10	E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 <i>ACM 1-58113-083</i> , pp. 214, 215.	
	D11	Darren JONES, "Opal Coprocessor Interface," <i>MIP Proprietary/Confidential</i> , June 4, 1999, pp. 1-18.	
	D12	Darren JONES, <i>MIPS64™ 5K™ Processor Cores User's Manual</i> , Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.	
	D13	<i>MIPS64™ 5K™ Processor Cores User's Manual</i> , Rev. 1.0.1., 1999, pp. 247-276.	
	D14	MIPS Technologies; "MIPS64 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82.	
	D15	MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide;" [Document No. MD00106]; ©1999-2001 <i>MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75.	
	D16	MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26.	

<b>Examiner Signature</b>		<b>Date Considered</b>	
---------------------------	--	------------------------	--

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number.

<sup>2</sup> Applicant is to place a check mark here if English language Translation attached.